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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/776,059
Filing Date	02-11-2004
First Named Inventor	Jaiprakash et al.
Art Unit	2186
Examiner Name	TBA
Attorney Docket Number	112020.143US2 NAN-19

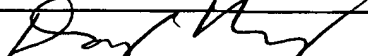
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**U. S. PATENT DOCUMENTS**

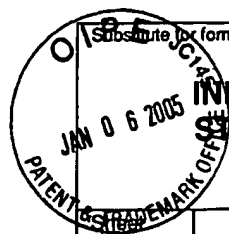
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
DM		US-6,574,130	09-04-2003	SEGAL et al.	
DM		US-6,643,165	11-04-2003	SEGAL et al.	
DM		US-6,706,402	03-16-2004	RUECKES et al.	
DM		US-6,784,028	08-31-2004	RUECKES et al.	
DM		US-6,759,693	07-06-2004	VOGELI et al.	
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DM		US-2002/0130311 A1	09-19-2002	LIEBER et al.	
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DM		US-6,559,468 B1	05-06-2003	KUEKES et al.	

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DM	US-6,750,471B2	06-15-2004	BETHUNE et al.
DM	US-6,159,620	12-12-2000	HEATH et al.
DM	US-6,198,655	03-06-2001	HEATH et al.
DM	US-6,232,706	05-15-2001	DAI et al.
DM	US-6,183,714	02-06-2000	SMALLEY et al.

**FOREIGN PATENT DOCUMENTS**

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DM		WO 01/44796 A1	06-21-2001	Board of Trustees of the Leland Stanford Junior University	
DM		WO 01/03208 A1	01-11-2001	President and Fellows of Harvard	

**NON PATENT LITERATURE DOCUMENTS**

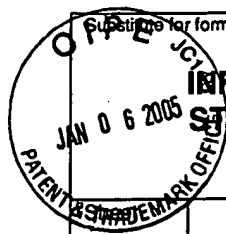
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DM	A1	CHOI, W.B. et al., "Carbon-nanotube-based nonvolatile memory with oxide-nitride-film and nanoscale channel," <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(2), pp.275-277.	
DM	A2	DEQUESNES, M. et al., "Calculation of pull-in voltages for carbon-nanotube-based nanoelectromechanical switches," <i>Nanotechnology</i> , 2002, Vol. 13, pp. 120-131.	
DM	A3	DEQUESNES, M. et al., "Simulation of carbon nanotube-based nanoelectromechanical switches," <i>Computational Nanoscience and Nanotechnology</i> , 2002, pp. 383-386.	
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Examiner Signature	Date Considered
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First Named Inventor	Jaiprakash et al.
Art Unit	2186
Examiner Name	TBA
Attorney Docket Number	112020.143US2 NAN-19

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of

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DM	A6	ROBINSON, L.A.W., "Self-Aligned Electrodes for Suspended Carbon Nanotube Structures," <i>Microelectronic Engineering</i> , 2003, Vols. 67-68, pp. 615-622.	
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DM	A12	SOH, H. T. et al., "Integrated nanotube circuits: Controlled growth and ohmic contacting of single-walled carbon nanotubes," <i>Appl. Phys. Lett.</i> , 1999. Vol. 75(5), pp. 627-629.	
DM	A13	KINARET, J.M. et al., "A carbon-nanotube-based nanorelay", <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(8), pp. 1287-1289.	
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DM	A15	AVOURIS, Ph., "Carbon nanotube electronics," <i>Chem. Physics</i> , 2002, Vol. 281, pp. 429-445.	
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Examiner Signature			Date Considered 12/10/05

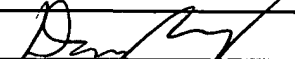
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		First Named Inventor	Jaiprakash et al.
		Art Unit	2186
		Examiner Name	TBA
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DM	A22	RADOSAVLJEVIC, M. et al., "Nonvolatile molecular memory elements based on ambipolar nanotube field effect transistors," <i>Nano Letters</i> , 2002, Vol. 2(7), pp. 761-764.	
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Jaiprakash, et al.

**Art Unit**

2891

**Examiner Name**

**Menz, Douglas M.**

Attorney Docket Number

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